

**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT***(use as many sheets as necessary)***Complete if Known**

Sheet 1 of 1	Application Number	10/761,702
	Filing Date	January 21, 2004
	First Named Inventor	Meng-Hung CHEN
	Art Unit	2815
	Examiner Name	Warren, Matthew E
Attorney Docket Number	10113671	

U.S. PATENT DOCUMENTS

Examiner Initials	Cite No.	Document Number (Number - kind code)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines Where Passages or Relevant Figures Appear
	A1	US-6,006,569	05-23-2000		
	A2				
	A3				
	A4				
	A5				
	A6				
	A7				

FOREIGN PATENT DOCUMENTS

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	B1					
	B2					

OTHER ART

Examiner Initials	Cite No.	Include name of author (in CAPS), title of article (when appropriate), title of the item (book, magazine, symposium, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published	T
MEW	C1	HONG XIAO, "Introduction to semiconductor manufacturing technology", 2001, pages 568-569, Prentice Hall Inc., USA	
MEW	C2	STANLEY WOLF, "Silicon Processing for the VLSI Era: Process Integration, Vol. 2", 1990, pages 214-217, Lattice Press, USA	
	C3		
	C4		
	C5		

Form PTO-1449 (modified)

(v08/25/2002)

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EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered.
Include copy of this form with next communication to applicant.

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*Matthew E Warren**10/11/05*